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| Form PTO 1449 (Rev. 2-32) U.S. Department of Commerce Patent and Trademark Office | Atty. Docket No. CISCO-7506 | Serial No. 10/651,596 |
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| **Information Disclosure Statement by Applicant** | Applicant: Daniel C. Biederman | |
| (Use several sheets if necessary) | Filed: 8/29/2003 | Group: 2116 |

**U.S. Patent Documents**

| Init. | | Document No. | Date | Name | Class | Subclass | Filing Date |
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**Foreign Documents**

| Init. | | Document No. | Date | Country | Class | Subclass | Translation Yes | Translation No |
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**Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)**

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| MRB | | Kiss, Peter, "Chapter 3: Cascaded Delta-Sigma ADCs", paper: Adaptive Digital Compensation of Analog Circuit Imperfections for Cascaded Delta-Sigma Analog-to-Digital Converters," Center for Design of Analog-Digital Integrated Circuits, Oregon State Univ., Dec. 31, 1999, pp.45-58. |
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| Examiner Michael D. B | Date Considered 6/16/06 |
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Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include a copy of this form with the next communication to applicant.